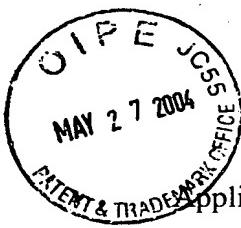


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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas Doris

Serial No.: 10/796,550

Title: Methods and Apparatus for Resource Allocation in Computer Architectures

Filed: March 9, 2004

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

Atty. Docket Number: 30320/17863

) I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date indicated: May 25, 2004.


Aaron M. Peters
Registration No. 48,801
Attorney for Applicant

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Copies of all non-U.S. patent documents and non-U.S. published patent application documents on the PTO-1449 form are attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.



TIME OF FILING

This information disclosure statement is being filed within three months of the filing date of the application or date of entry into the national stage of an international application. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 13-2855, under Order No. 30320/17863. A duplicate copy of this paper is enclosed.

Dated: May 25, 2004

Respectfully submitted,

By:


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For PTO-1493 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 30320/17863	Serial No. 10/796,550
		Applicant	
		Thomas Doris	
		Filing Date March 9, 2004	Group Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
	Intel® Technology Journal, Network Processors, The Next Generation of Intel IXP Network Processor, Volume 06, Issue 03, Pages 6 - 18, Copyright® Intel Corporation 2002.
	“Introduction to the Auto-Partitioning Programming Model,” Accelerating Custom Application Development on Intel® IXP2XXX Network Processors, 10 pages, © 2003.

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.